

2133
4
137

10-02-02



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Vitaly LAGOON et al.

Serial No.: 09/939,743

Filed: August 28, 2001

For: Method For Providing Bitwise Constraints
For Test Generation

Examiner:

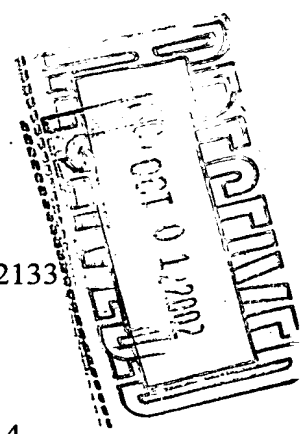
§
§
§
§
§
§
§
§
§
§

Group Art Unit: 2133

Attorney

Docket: 24771

Previously V02/14



INFORMATION DISCLOSURE STATEMENT

Director of the United States Patent and Trademark Office
US Patent and Trademark Office
Washington, D.C. 20231

RECEIVED

SEP 25 2002

Technology Center 2100

Sir:

Enclosed is a PTO Form SB/08A which lists citations which may be material to the patentability and examination of the above identified application. Also enclosed are copies of the references cited. These are submitted in compliance with the duty of disclosure defined in 37 CFR 1.56. The Examiner is requested to make these citations of official record in this application.

This Information Disclosure Statement under 37 CFR 1.56 is not to be construed as a representation that a search has been made, that additional matter which is material to the examination of this application does not exist, or that any or more of these citations constitutes prior art.

Respectfully submitted,

Sol Sheinbein
Registration No. 25,457
Attorney for Applicant

Date: September 18, 2002

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.**